



PATENT

IN THE UNITED STATES PATENT OFFICE

Applicants: Guenter Stenz et al.  
Assignee: Xilinx, Inc.  
Title: Method System and Apparatus for Floorplanning Programmable Logic Designs  
Serial No.: 10/787,326 File Date: 02-26-04  
Examiner: Vuthe Siek Art Unit: 2825  
Docket No.: X-1478 US Conf. No.: 3972

Mail Stop AMENDMENT  
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INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicants bring to the attention of the Examiner the three (3) references listed in the attached Substitute for Form 1449A/PTO. A copy of each is enclosed herein.

The enclosed references are being cited after the receipt of a first office action but prior to any final action.

Citation of the above documents shall not be construed as an admission that the documents are necessarily prior art with respect to the instant invention. Citation of the above documents shall not be construed as a representation that a search has been made other than as described above. Also, the citation of the above documents shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

The Commissioner is hereby authorized to charge the fee of \$180.00 due under 1.17(p) and any additional fees, or credit any overpayment which may be required to Deposit Account No. 24-0040.

Respectfully submitted,

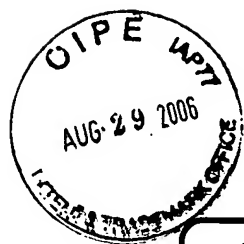
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*I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450, on August 23, 2006.*

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PTO/SB/08A (10-01)  
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Substitute for form 1449A/PTO		<b>Complete if Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)		Application / Conf. No.	10/787,326 / 3972
		Filing Date	February 26, 2004
		First Named Inventor	Gunter Stenz
		Art Unit	2825
		Examiner Name	Vuthe Siek
Sheet 1 of 1	Attorney Docket Number	X-1478 US	

OTHER -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		Hiroshi Murata et al.; "VLSI Module Placement Based on Rectangle-Packing by the Sequence-Pair"; IEEE Transactions on computer-Aided Design of Integrated Circuits and Systems; Vol. 15, No. 2; December 1996; pp. 1518-1524.	
		S. Kirkpatrick et al.; "Optimization by Simulated Annealing"; Science; Volume 220, Number 4598; 13 May 1983; pp. 671-680.	
		Dieter Jungnickel; Graphs, Networks and Algorithms; "Algorithms and Computation in Mathematics"; Chapter 13 - Weighted Matchings; Volume 5; Copyright 1999; published by Springer-Verlag Berlin Heidelberg New York; pp. 389-401.	

Examiner Signature		Date Considered	
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Applicant's unique citation designation number. <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached.

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